

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Bernd Deutschmann, et al.	Art Unit	: 2836
Serial No.	: 10/588,984	Examiner	: Dharti H. Patel
Filed	: November 21, 2006	Conf. No.	: 4272
Title	: CIRCUIT ARRANGEMENT AND METHOD FOR PROTECTING AN INTEGRATED SEMICONDUCTOR CIRCUIT		

**Mail Stop Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF MARCH 12, 2009

Please amend the above-identified application as follows.